

Design and Implementation of Intelligent Car Washing System with Water Saving Technique

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Abstract: This paper discusses an FPGA-based intelligent car washing system. The system provides washing services that are rapid, convenient, and effective. A flow chart for controlling an automated car washing system with four working modes is given in this paper. The car washing system was built with the help of Xilinx Vivado in the software ISE Design Suite using the hardware description language Verilog HDL. In this project, a special feature for water conservation by biologically recycling the water is implemented.

Keywords: FPGA, Intelligent car washing system, Verilog HDL

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