IJARSCT



International Journal of Advanced Research in Science, Communication and Technology (IJARSCT)

Volume 2, Issue 3, May 2022

Islanding Scheme for Microcontroller Based Load Frequency Controller

Ms. Suchita V. Patil, Ms. Sayli M. Surve, Ms. Sayali A. Patil, Ms. Maheshwari M. Babar Students, Department of Electrical Engineering AISSMS Institute of Information Technology, Pune, Maharashtra, India

Abstract: This paper is for detecting power sync errors. The actual power grid consists of power sources that are synchronously connected to power the system. These power supplies must be powered according to grid rules. These rules include voltage and frequency fluctuations within certain limits. Deviations from these limits require that this data source be immediately disconnected from the network and affected. This is known as solo operation. It is used to avoid large voltage drops or power outages from the grid. Our system is designed to warn the network of a power failure, so the network can use other backup data sources as needed to avoid a complete power failure. Our system demonstrates this with the ATmega328P microcontroller. MC is used to detect voltage and frequency from a set of comparators. Since the frequency cannot be changed, use the frequency generator (555 timer). Standard variacs are also used to vary the input voltage of the system. Normal loads are used to indicate the expected power outage or power outage if the voltage / frequency deviation exceeds the limit.

Keywords: Synchronization, Islanding, Voltage and Frequency etc.

REFERENCES

- [1]. Subhodeep Joshi, Tushar Parihar, Varun Kumar Shakya, Pradeep Kumar, Upendra Pal Singh "Detection of Power Grid Synchronization Failure on Sensing Frequency and Voltage Beyond Acceptable Range" Volume 6, April 2017, International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering.
- [2]. Naga Sarvani, B. Vineela Thulasi, K. Rahul, K. Satish Kumar, V.D. Sekhara Varma "Detection of Power Grid Synchronization Failure on Sensing Frequency and Voltage Beyond Acceptable Range and Load Protection" volume 4, July 2017, International Research Journal of Engineering and Technology (IRJET).
- [3]. Aman Pratap Singh, Vivek Kumar, Shreya Chaudhary, Prabhat Singh, and Chandra Shekhar Singh "Power Grid Synchronization Failure Detection" volume 4, March 2017 Journal of Energy Research and Environmental Technology (JERET).

DOI: 10.48175/IJARSCT-3852