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# Power-Efficient VLSI Design using Dynamic Voltage and Frequency Scaling for IoT Devices

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**Abstract:** *In the rapidly evolving landscape of Internet of Things (IoT) devices, the demand for power-efficient solutions has become paramount. This paper explores the integration of Dynamic Voltage and Frequency Scaling (DVFS) into Very Large Scale Integration (VLSI) design to enhance the power efficiency of IoT devices. Through a comprehensive analysis of the system architecture, implementation of DVFS, and experimental results, this research sheds light on the potential of this approach to address the energy challenges faced by IoT devices..*

**Keywords:** Internet of Things

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